

Fig. 1 Prior art

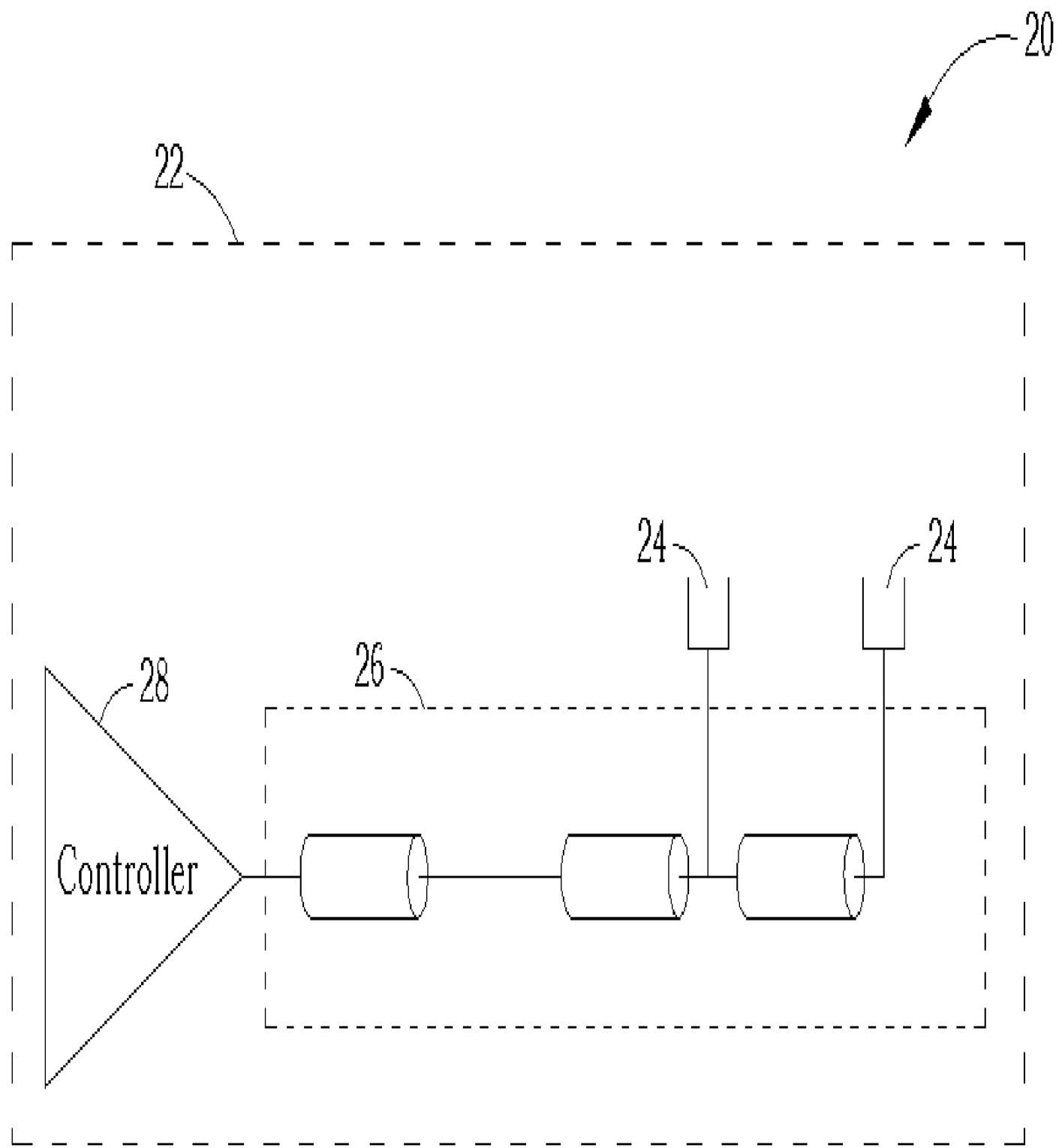


Fig. 2 Prior art

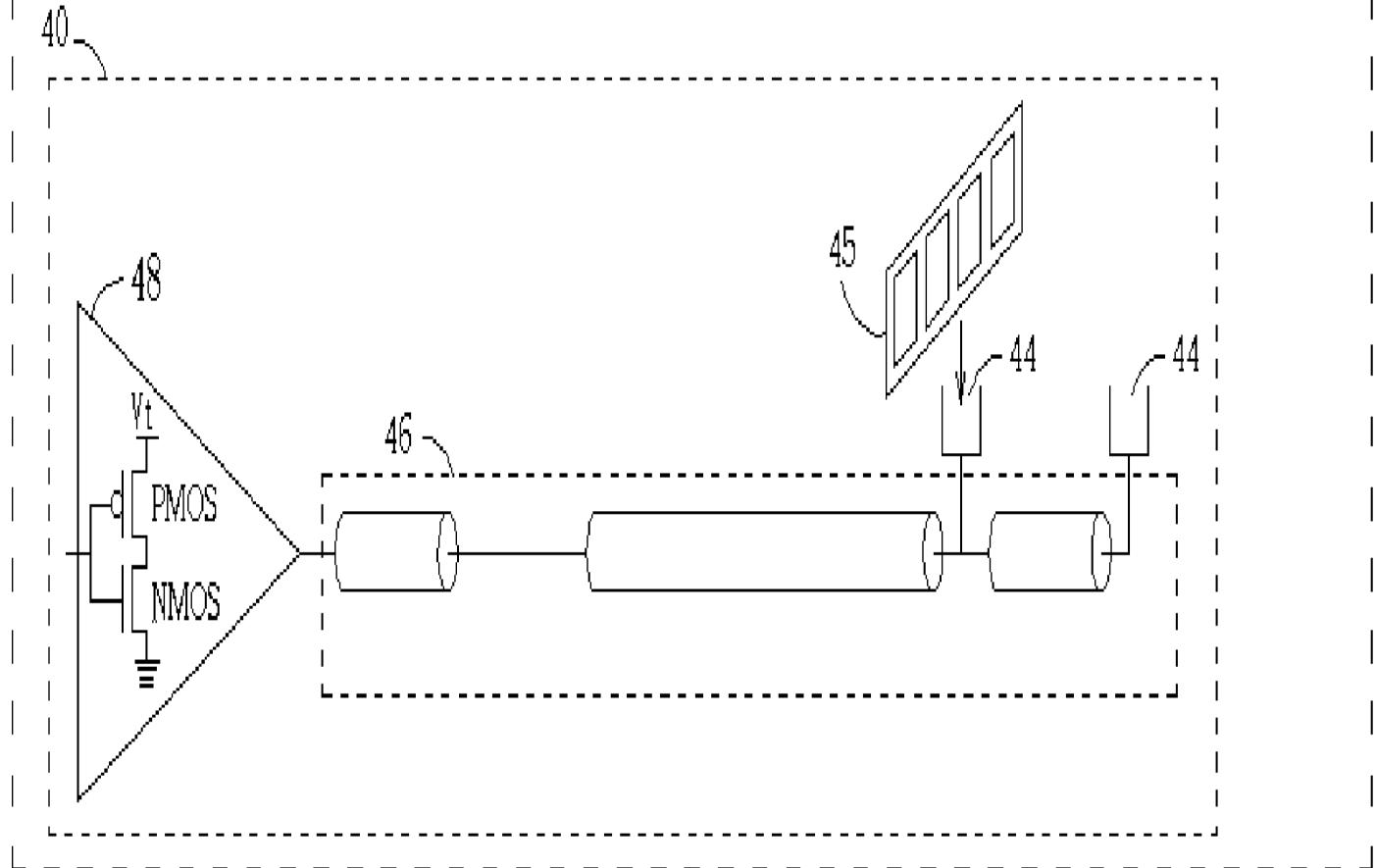
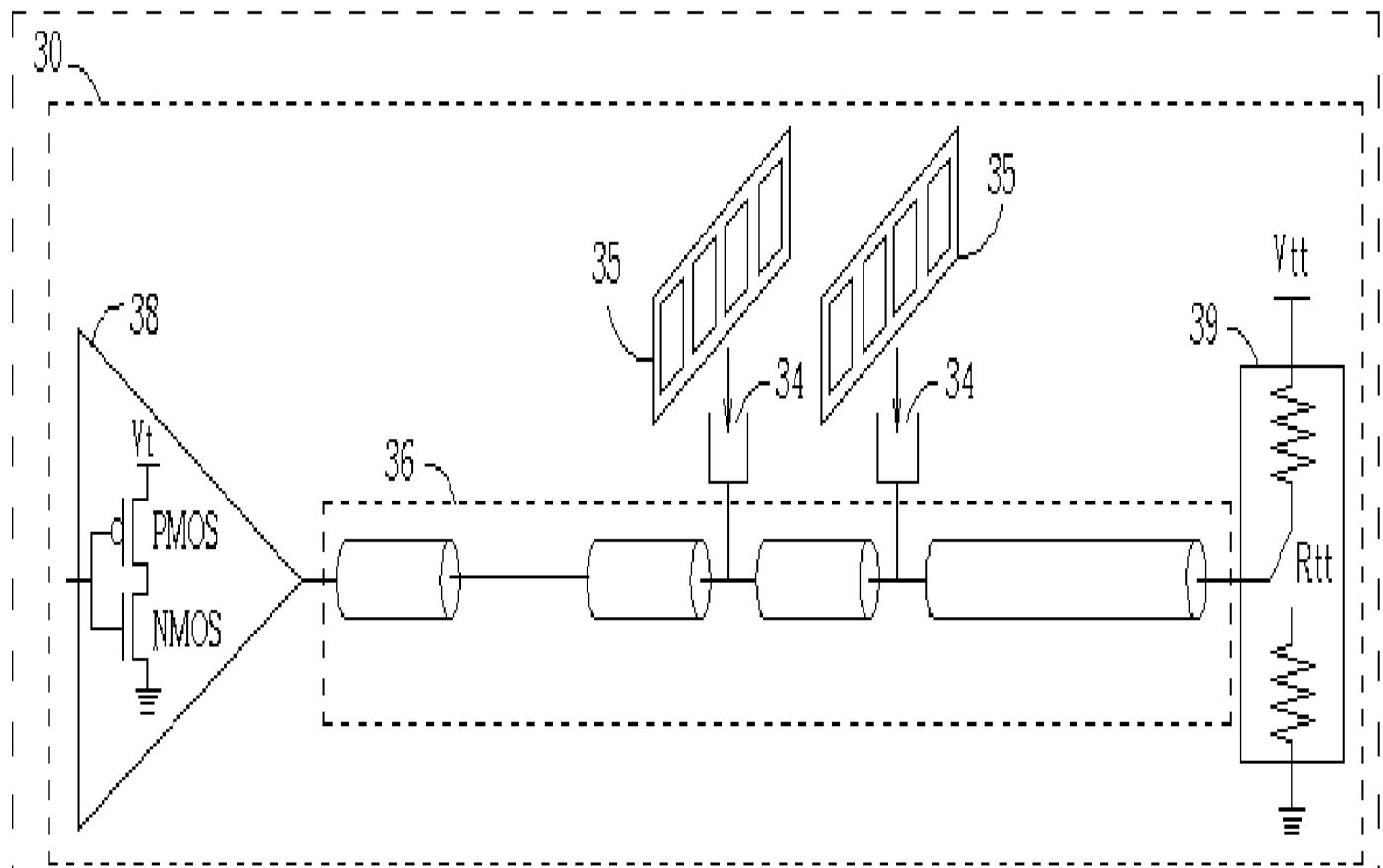


Fig. 3

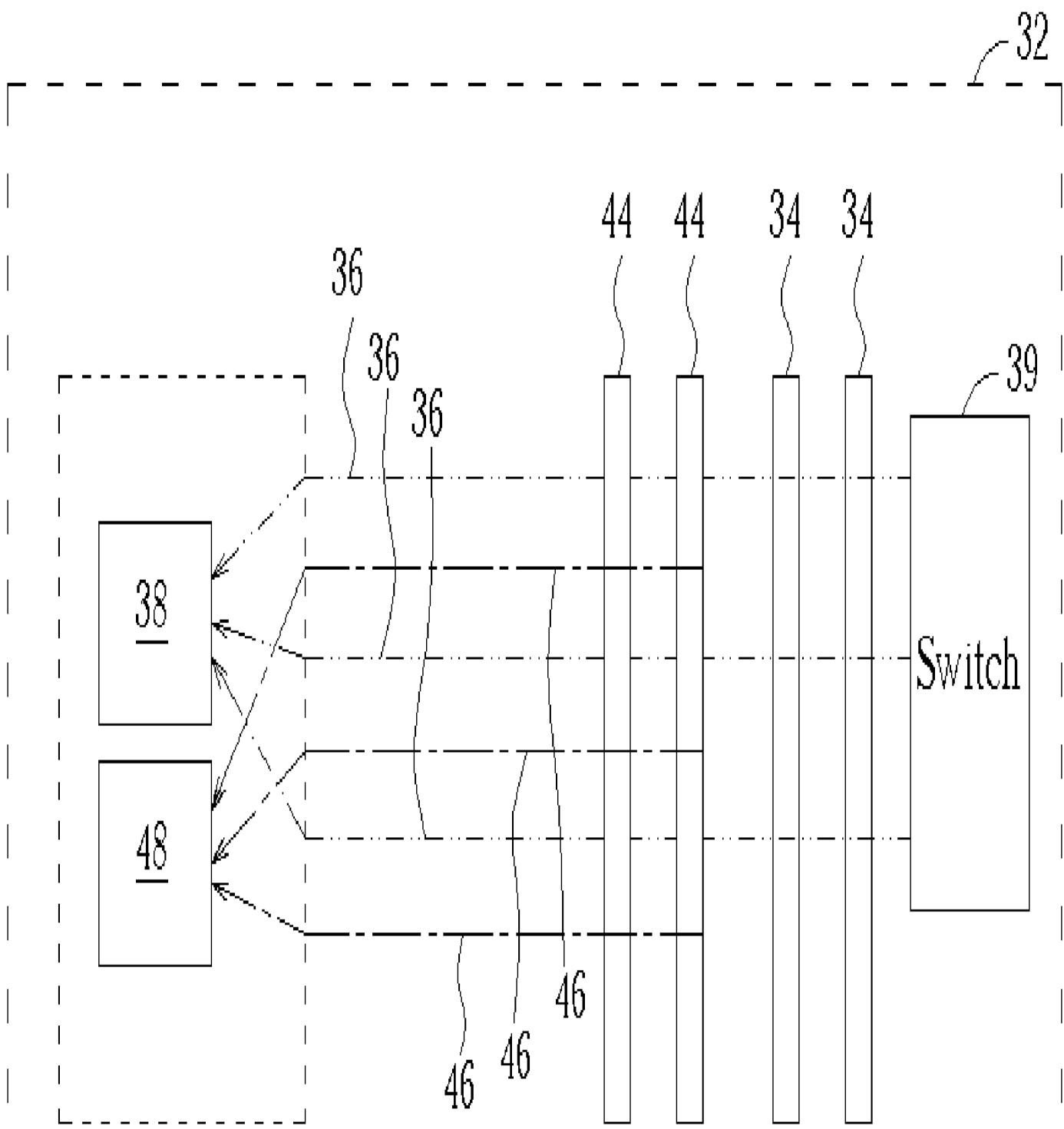


Fig. 4

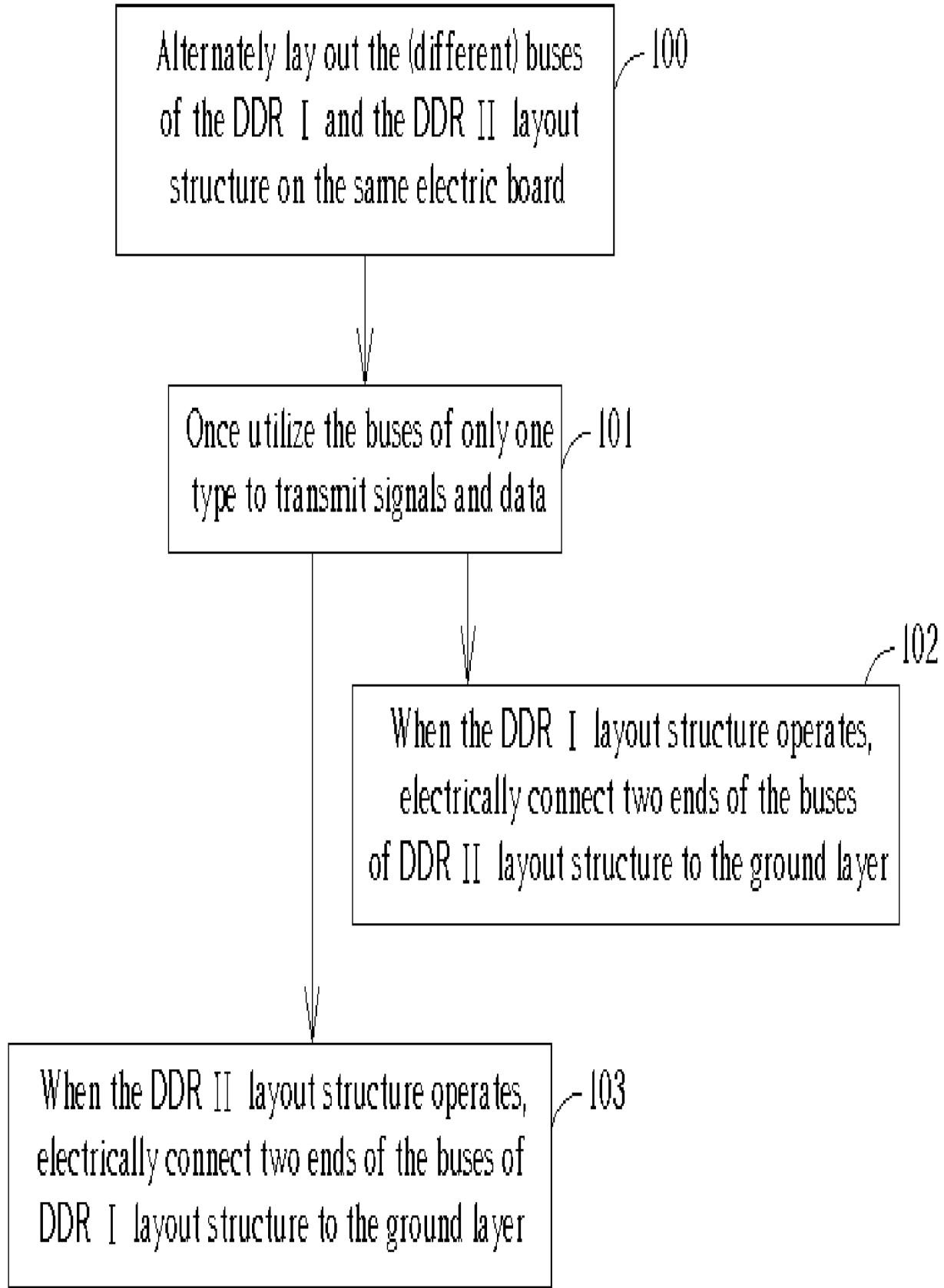


Fig. 5

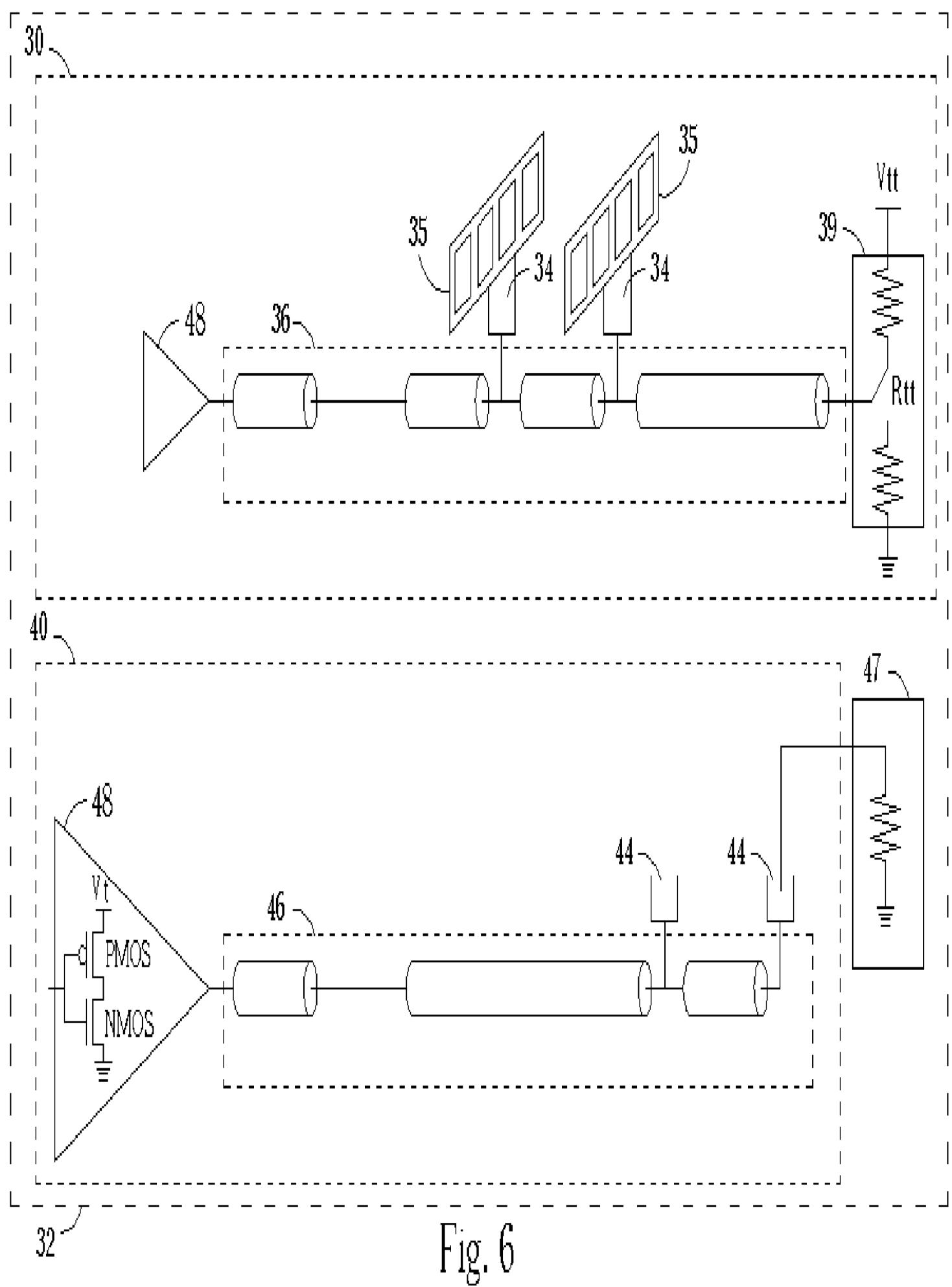
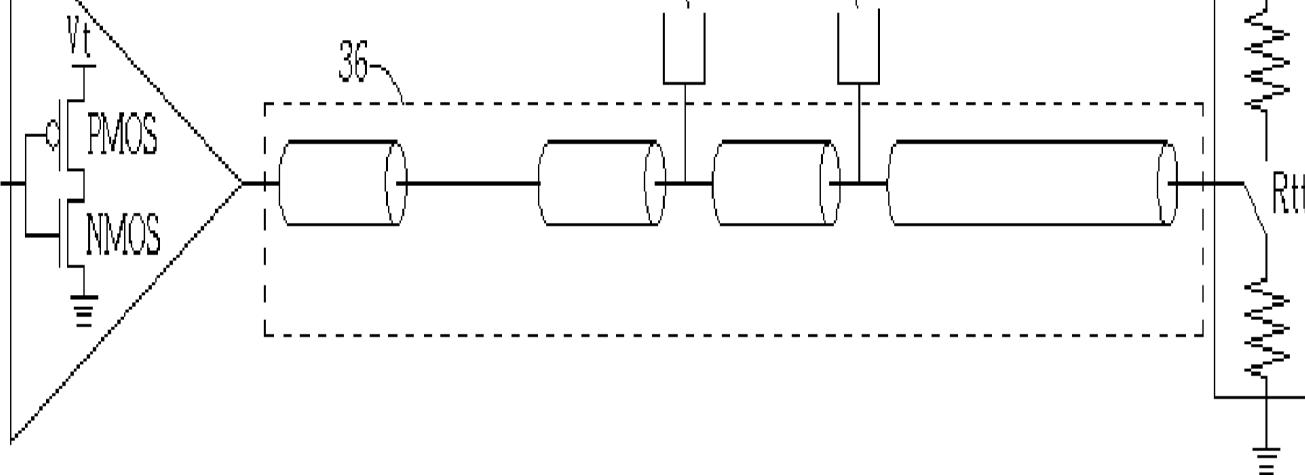
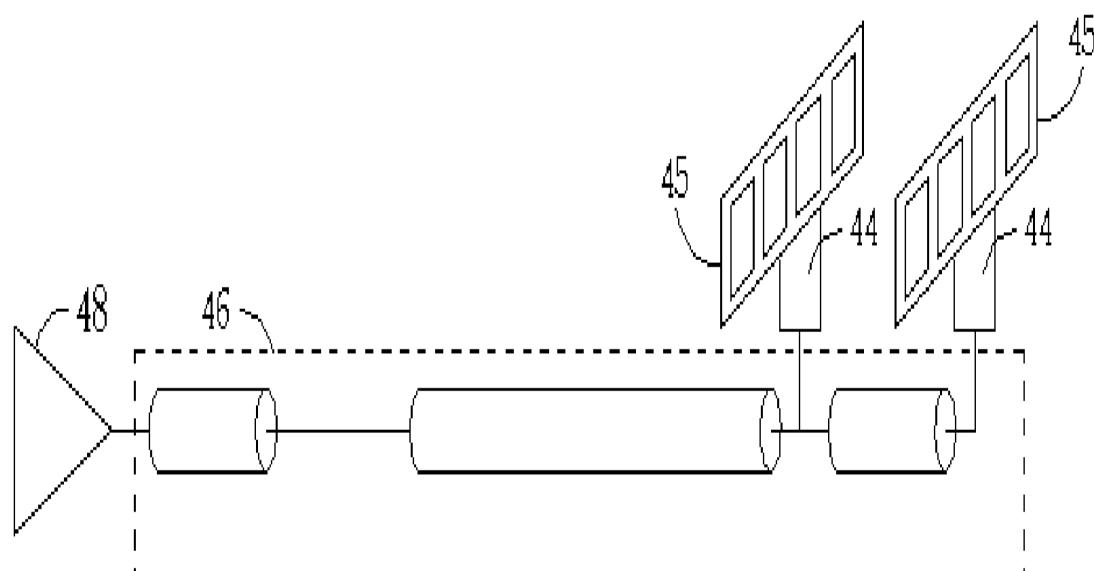


Fig. 6

38

PMOS
NMOS

40



32

Fig. 7